

Specification

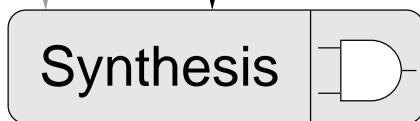
Microarchitecture Library
Macro Blocks



(.vhd)

Functional Sim.

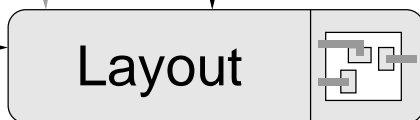
Standard Cells Library
Design Constraints
Timing Models



(.v)

Gatelevel Sim.
Timing Analysis

Technology Files
Floorplan / Powerplan
Geometry Informations



(.gds)

Backannotated Sim.
Timing Analysis

Layout-
Verifications

GDS